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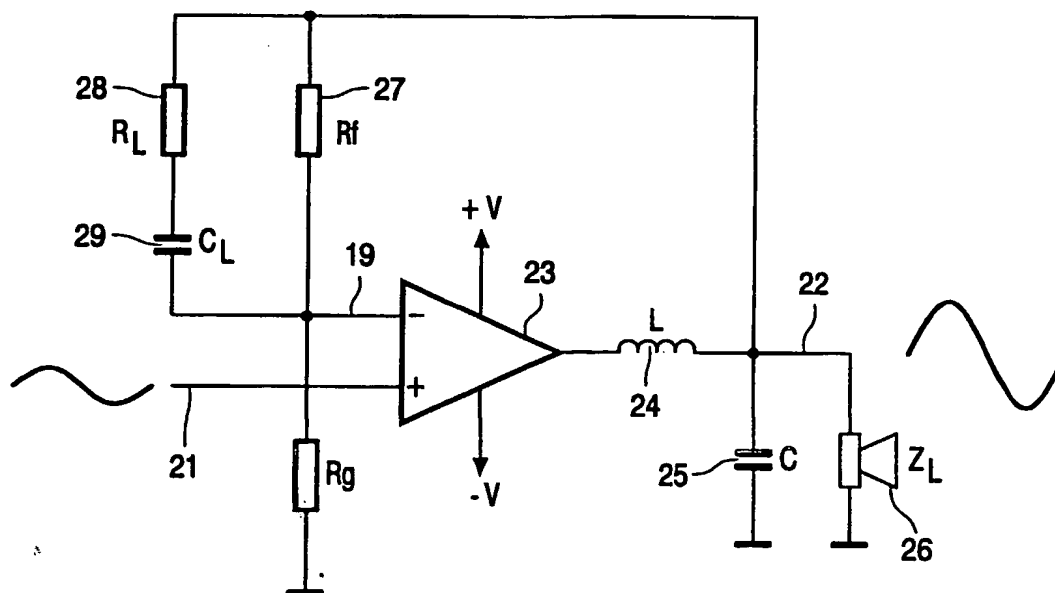
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(54) Title: POWER AMPLIFIER



(57) Abstract: A switching power amplifier of class D has switching means for generating a block wave signal by alternately switching the block wave signal between supply voltages. A filter (24,25) generates a power output signal (22) corresponding to an input signal to be amplified, which is coupled to a linear input (19) that is free of hysteresis. A control circuit (27,28,29) provides feedback between the output power signal and the linear input for controlling both the gain in the operational frequency range and also said alternately switching of the switching means. Hence the amplifier oscillates controlled via the same feedback loop that also controls the operational behavior of the amplifier, resulting in low output impedance and low distortion.

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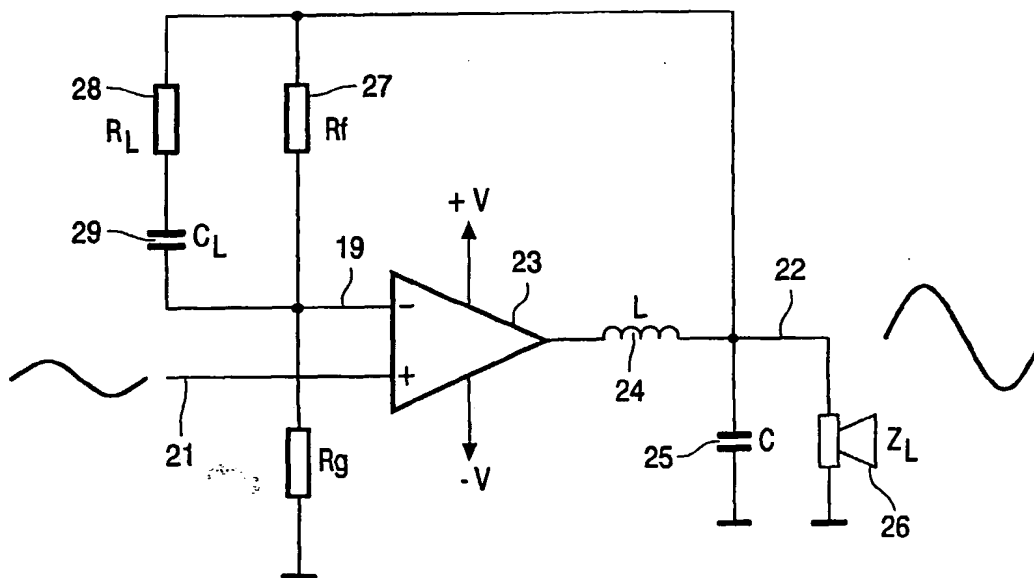
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Y	US 5 722 052 A (ABDI BEHROOZ L ET AL) 24 February 1998 (1998-02-24) column 4, line 40 - column 8, line 17; figure 6	6
Y	FR 1 602 588 A (SESCO) 28 December 1970 (1970-12-28) page 6, line 6 - page 7, line 40; figure 4	8
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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